



# DSD 2017

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# 20<sup>th</sup> Euromicro Conference on Digital System Design

Vienna, Austria, Aug. 30th - Sept. 1st, 2017

## Call for Papers



SPECIAL SESSION ON

# ARCHITECTURES & SYSTEMS FOR AUTOMOTIVE & INTELLIGENT TRANSPORTATIONS ASAIT

## SPECIAL SESSION SCOPE

Digital systems in intelligent transportation systems (ITS) will play an important role and are becoming increasingly complex. New applications for these ITS have emerged to respond to people and societal needs and to ensure driver and passenger safety. Applications such as Driverless cars, Vehicle-to-X (V2X) communications, in-vehicle infotainment, and assistance for elderly and disabled drivers require powerful processing and communication capabilities. Moreover, modern transport tools play the leading role in strategically worldwide projects such as green and sustainable mobility in future smart cities and smart roads. To fulfill these tasks, the number and types of sensors in next generation of transportations tools will continually increase and requires significant computing power, low energy consumption and high reliability and security.

The purpose of this special session is to look deeper into these issues, and bring researchers and practitioners from industry and academia to explore and discuss recent progress, and stimulate the interaction between them through exchange of ideas and experience sharing.

Topics of interest include, but are not limited to:

- System architecture & software design for ITS: Multicore, FPGA, GP-GPU and heterogeneous system challenges
- Safety, Security and Reliability. Assessment and detection of security threats and vulnerabilities
- Designing architecture and software for security
- Interfaces & Driver Assistance Systems, and in-vehicle networks (Most, Flexray, Can, Ethernet, etc.)
- Standards (AUTOSAR, ISO, EN, etc.) deployments
- HMI, infotainment development and usability testing in ITS
- Development methods for advanced driver assistance systems
- Design methods and tool-chains for embedded systems in autonomous vehicles and active safety
- Architectural design and optimization for EVs and HEVs
- Infrastructure analysis, modeling, simulation and optimization in ITS
- Energy-efficient vehicles, connected vehicles and infrastructure design, energy generation and saving
- Hardware and software solutions for run-time system management, self-diagnostics and repair
- New architectures, protocols and implementations for communicating vehicles (V2X) and infrastructures
- Digital systems and circuits for smart cities and smart roads

## SUBMISSION GUIDELINES

Authors are encouraged to submit their manuscripts to <https://easychair.org/conferences/?conf=dsd2017>. Should an unexpected web access problem be encountered, please contact the Program Chair by email ([dsd2017@easychair.org](mailto:dsd2017@easychair.org)).

Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors' names should appear in the manuscript, references included.

IEEE Conference Publishing Services (CPS) will publish accepted papers in the conference proceedings and the proceedings will be submitted to the IEEE Xplore Digital library and indexing services. Extended versions of selected best papers will be published in a special issue of the ISI indexed "Microprocessors and Microsystems: Embedded Hardware Design" Elsevier journal.

## IMPORTANT DATES

Deadline for paper submission: April 15th  
Notification of acceptance: June 10th  
Camera ready papers: July 1st

## MORE INFORMATION (WEB PAGES)

- DSD 2017: <http://dsd-seaa2017.ocg.at>
- Euromicro: [www.euromicro.org](http://www.euromicro.org)